Abstracting Abstract Control

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Abstract

The strength of a dynamic language is also its weakness: run-time flexibility comes at the cost of compile-time predictability. Many of the hallmarks of dynamic languages such as closures, continuations, various forms of reflection, and a lack of static types make many programmers rejoice, while compiler writers, tool developers, and verification engineers lament. The dynamism of these features simply confounds statically reasoning about programs that use them. Consequently, static analyses for dynamic languages are few, far between, and seldom sound.

The "abstracting abstract machines" (AAM) approach to constructing static analyses has recently been proposed as a method to ameliorate the difficulty of designing analyses for such language features. The approach, so called because it derives a function for the sound and computable approximation of program behavior starting from the abstract machine semantics of a language, provides a viable approach to dynamic language analysis since all that is required is a machine description of the interpreter.

The AAM recipe as originally described produces finite state abstractions: the behavior of a program is approximated as a finite state machine. Such a model is inherently imprecise when it comes to reasoning about the control stack of the interpreter: a finite state machine cannot faithfully represent a stack. Recent advances have shown that higher-order programs can be approximated with pushdown systems. However, such models, founded in automata theory, either breakdown or require significant engineering in the face of dynamic language features that inspect or modify the control stack.

In this paper, we tackle the problem of bringing pushdown flow analysis to the domain of dynamic language features. We revise the abstracting abstract machines technique to target the stronger computational model of pushdown systems. In place of automata theory, we use only abstract machines and memoization. As case studies, we show the technique applies to a language with closures, garbage collection, stack-inspection, and first-class composable continuations.

1. Introduction

Good static analyses use a combination of abstraction techniques, economical data structures, and a lot of engineering [7, 39]. The cited exemplary works stand out from a vast amount of work attacking the problem of statically analyzing languages like C. Dynamic languages do not yet have such gems. The problem space is different, bigger, and full of new challenges. The traditional technique of pushing abstract values around a graph to get an analysis will not work. The first problem we must solve is, "what graph?" as control-flow is now part of the problem domain. Second, features like stack inspection and first-class continuations are not easily shoe-horned into a CFG representation of a program's behavior. We need a new approach.

Luckily, there is an alternative to the CFG approach to analysis construction that is based instead on abstract machines, which are one step away from interpreters (they are interderivable in several instances [8]). This alternative, called abstracting abstract machines (AAM) [34], is a simple idea that is generally applicable to even the most dynamic of languages, *e.g.*, JavaScript [19]. A downside is that all effective instantiations of AAM are finite state approximations. Finite state techniques cannot precisely predict where a method or function call will return. Dynamic languages have more sources for imprecision than non-dynamic languages (*e.g.*, reflection, computed fields, runtime linking, `eval`) that all need proper treatment in the abstract. If we can't have precision in the presence of statically unknowable behavior, we should at least be able to *contain* it in the states it actually affects. Imprecise control flow due to finite state abstractions is an unacceptable containment mechanism. It opens the flood gate to imprecision flowing everywhere through analyses' predictions. It is also a solvable problem.

We extend the AAM technique to computably handle infinite state spaces by adapting pushdown abstraction methods to abstract machines. The unbounded stack of pushdown systems is the mechanism to precisely match calls and returns. We demonstrate the essence of our pushdown analysis construction by first applying the AAM technique to a call-by-value functional language (§2) and then revising the derivation to incorporate an exact representation of the control stack (§3). We then show how the approach scales to stack-reflecting language features such as garbage collection and stack inspection (§4), and stack-reifying features in the form of first-class delimited control operators (§5). These case studies show that the approach is robust in the presence of features that need to inspect or alter the run-time stack, which previously have required significant technical innovations [17, 36].

Our approach appeals to operational intuitions rather than automata theory to justify the approach. The intention is that the only prerequisite to designing a pushdown analysis for a dynamic language is some experience implementing interpreters; expertise in automata theory or abstract domains is unneeded. With simple but powerful tools in a large audience's toolbelt, we hope this work will help the community work towards technical feats like Astrée [7]. The large problem domain of analyzing dynamic languages needs an army to tackle it, and we believe that AAM can feed that army.

2. The AAM methodology

Abstract machines are a versatile, clear and concise way to describe the semantics of programming languages. They hit a sweet spot in terms of level of discourse, and viable implementation strategy. First year graduate students learn programming language semantics with abstract machines. They also turn out to be fairly simple to repurpose into static analyses for the languages they implement, via the Abstracting Abstract Machines methodology [33]. The basic idea is that abstract machines implement a language's *concrete* semantics, so we transform them slightly so that they also implement a language's *abstract* semantics (thus "abstracting" abstract machines).

AAM is founded on three ideas:

1. concrete and abstract semantics ideally should use the same code, for correctness and testing purposes,
2. the level of abstraction should be a tunable parameter,
3. both of the above are achievable with a slight change to the abstract machine's state representation.

The first two points are the philosophy of AAM: correctness through simplicity, reusability, and sanity checking with concrete semantics. The final point is the machinery that we recount in this section.

2.1 The $CESK_t^*$ machine *schema*

The case studies in this paper have full implementations in PLT redex [12] available online.[1] They all build off the call-by-value untyped lambda calculus, whose semantics we recall in small-step reduction semantics style:

$$E[(\lambda x.e\ v)] \longmapsto_\beta E[[v/x]e]$$

where $E \in \mathit{EvaluationContext} ::= [\,] \mid (E\ e) \mid (v\ E)$

$e \in \mathit{Expr} ::= x \mid (e\ e) \mid v \quad v \in \mathit{Value} ::= \lambda x.e$

Although concise and well-structured for mathematical proofs, this semantics does not reflect what an effective implementation looks like. The context decomposition to find a redex (left hand side of $\longmapsto_\beta$), substitution and re-plugging are better understood as a process that a machine goes through to perform a step. We thus start with an abstract machine that manages these steps with a special state representation: the CESK machine.[2] The environment and store work together to represent ongoing substitutions, and the continuation represents where evaluation is with respect to decomposing and re-plugging a context. The state space is in Figure 1. The standard CESK machine has a store that maps freshly allocated addresses to single values. An address a is "fresh" if $a \notin \mathrm{dom}(\sigma)$, but this is just a specification, not an implementation. Concretely, there is an allocation function $\mathit{alloc} : \mathit{CESK} \to \mathit{Addr}$ that will produce an address for the machine to use. We use a slightly non-standard CESK machine that allows alloc to produce arbitrary addresses instead of just fresh ones.

Allocation is key for both precision and abstraction power. Fresh allocation gives us the full lambda calculus – everything is undecidable. If the allocator has a finite codomain, and there are no recursive datatypes in a state's representation, then the state space is finite – the machine is a finite state machine. If the allocator freshly allocates addresses for the stack representation, but is finite for everything else, the semantics is indistinguishable from just using the recursive representation of the stack. If we have just the stack as an unbounded data structure, then the machine has finitely many stack frames and finitely many other components of the state – the machine is a pushdown system.

AAM slightly modifies the store to map arbitrarily allocated addresses (reuse allowed) to a set of values. That way, if an address is reused then the previous tenant of the address is not forgotten, just merged as a *possible* result when the address is dereferenced. The allocation function is a *parameter*; the "machine" AAM produces is really a machine *schema*, since we can vary the allocator to produce both and anything in between a concrete and abstract semantics. That said, we will still refer to the refactored semantics as machines going forward. The semantics of this CESK machine is shown in Figure 2. The only differences from the standard

$$\begin{array}{rl}
\varsigma \in \mathit{CESK} & ::= \langle e, \rho, \sigma, \kappa\rangle \\
\ell \in \mathit{Lam} & ::= \lambda x.e \qquad\qquad v \in \mathit{Value} ::= (\ell, \rho) \\
\rho \in \mathit{Env} & = \mathit{Var} \rightharpoonup \mathit{Addr} \qquad \sigma \in \mathit{Store} = \mathit{Addr} \rightharpoonup \wp(\mathit{Value}) \\
\phi \in \mathit{Frame} & ::= \mathbf{appL}(e, \rho) \mid \mathbf{appR}(v) \\
\kappa \in \mathit{Kont} & = \mathit{Frame}^* \\
x \in \mathit{Var} & \text{ a set} \qquad\qquad a, b \in \mathit{Addr} \text{ a set} \\
\multicolumn{2}{r}{\mathit{alloc} : \mathit{CESK} \to \mathit{Addr}}
\end{array}$$

Figure 1. CESK semantic spaces

$\varsigma \longmapsto \varsigma'$	$a = \mathit{alloc}(\varsigma)$
$\langle x, \rho, \sigma, \kappa\rangle$	$\langle v, \sigma, \kappa\rangle$ if $v \in \sigma(\rho(x))$
$\langle (e_0\ e_1), \rho, \sigma, \kappa\rangle$	$\langle e_0, \rho, \sigma, \mathbf{appL}(e_1, \rho) : \kappa\rangle$
$\langle v, \sigma, \mathbf{appL}(e, \rho) : \kappa\rangle$	$\langle e, \rho, \sigma, \mathbf{appR}(v) : \kappa\rangle$
$\langle v, \sigma, \mathbf{appR}(\lambda x.e, \rho) : \kappa\rangle$	$\langle e, \rho[x \mapsto a], \sigma \sqcup [a \mapsto v], \kappa\rangle$

Figure 2. CESK machine

$$\begin{array}{rl}
\hat{\varsigma} \in \widehat{\mathit{CESK}}_t & ::= \langle e, \rho, \hat{\sigma}, \hat{\kappa}\rangle_t \\
\hat{\sigma} \in \widehat{\mathit{Store}} & = \mathit{Addr} \to \wp(\mathit{Storeable}) \qquad \hat{\kappa} \in \widehat{\mathit{Kont}} ::= \epsilon \mid \phi{:}a \\
t, u \in \mathit{Time} & \qquad\qquad \mathit{Storeable} ::= \hat{\kappa} \mid v
\end{array}$$

Figure 3. $CESK_t^*$ semantic spaces

presentation are in the use of $\in$ instead of $=$ to make lookups non-deterministic, and weak updates with $\sqcup$ instead of strong updates to allow sound reuse of addresses:

$$\begin{aligned}
\sigma \sqcup [a \mapsto v] &= \sigma[a \mapsto \sigma(a) \sqcup v] \\
v \sqcup v' &= \{v, v'\} \\
\{v, \ldots\} \sqcup v' &= \{v, \ldots, v'\}
\end{aligned}$$

Removing recursion: In order to make $\longmapsto$ finite and therefore have a computable graph, we must finitize the recursive spaces in which the machine *creates new values*. AAM dictates that finitization can be centralized to one place, address allocation, by redirecting values in recursive positions through the store. Expressions, though recursive, do not need this step because they are only destructed. The Expr space is finite for each program, the size of which is the number of subexpressions in the program.

The runaway recursion is in Kont: the tail of the continuation cons is recursive. So then conses of frames in Kont instead allocate an address, update the heap with the recursive value, and use the address in place of the recursive value. To help an $\mathit{alloc} : \mathit{State} \to \mathit{Addr}$ function choose its addresses, the state space can be extended with an arbitrary pointed space that can be updated each step. The original AAM paper calls this pointed space and update function (Time, t_0) and tick respectively. The point, t_0, is for the initial state's timestamp. Later work on widening [15] suggests less arbitrary constructions, and to think of Time as a space of abstract traces, though the "abstraction" need not be sound [23]. The timestamp machinery is important to implementing specific allocation schemes, but is primarily noise for the exposition in this paper, so we omit it past the first couple demonstrations.

The new semantic spaces in Figure 3 form the $CESK_t^*$ machine. The semantics of this machine follow the weak update and non-deterministic lookup principles of AAM in Figure 4.

If we run the $CESK_t^*$ semantics to explore all possible states, we get a sound approximation of all paths that the CESK machine will explore. This paper will give a more focused view of the Kont component. We said that when just Kont is unbounded, we have a pushdown system. Pushdown systems cannot be naively

[1] `http://github.com/dvanhorn/aac`

[2] CESK stands for control string (or code), environment, store, and continuation (but with a k to distinguish from the first c). The continuation is sometimes called the stack.

$\hat{\varsigma} \longmapsto \hat{\varsigma}' \quad a = alloc(\hat{\varsigma}) \quad u = tick(\hat{\varsigma})$

$\langle x, \rho, \hat{\sigma}, \hat{\kappa}\rangle_t$	$\langle v, \hat{\sigma}, \hat{\kappa}\rangle_u$ if $v \in \sigma(\rho(x))$
$\langle (e_0\ e_1), \rho, \hat{\sigma}, \hat{\kappa}\rangle_t$	$\langle e_0, \rho, \hat{\sigma}', \mathbf{appL}(e_1, \rho){:}a\rangle_u$
where	$\hat{\sigma}' = \hat{\sigma} \sqcup [a \mapsto \hat{\kappa}]$
$\langle v, \rho, \hat{\sigma}, \mathbf{appL}(e, \rho'){:}b\rangle_t$	$\langle e, \rho', \hat{\sigma}, \mathbf{appR}(v, \rho){:}b\rangle_u$
$\langle v, \hat{\sigma}, \mathbf{appR}(\lambda x.e, \rho){:}b\rangle_t$	$\langle e, \rho', \hat{\sigma}', \hat{\kappa}\rangle_u$ if $\hat{\kappa} \in \hat{\sigma}(b)$
where	$\rho' = \rho[x \mapsto a]$
	$\hat{\sigma}' = \hat{\sigma} \sqcup [a \mapsto v]$

Figure 4. $CESK_t^*$ semantics

run to find all states and describe all paths the $CESK$ machine can explore; the state space is infinite, therefore this strategy may not terminate. The pushdown limitation is special because we can always recognize non-termination, stop, and describe the entire state space. We show that a simple change in state representation can provide this functionality. We regain the ability to just *run* the semantics and get a finite object that describes all possible paths in the $CESK$ machine, but with better precision than before.

3. A refinement for exact stacks

We can exactly represent the stack in the $CESK_t^*$ machine with a modified allocation scheme for stacks. The key idea is that if the address is "precise enough," then every path that leads to the allocation will proceed exactly the same way until the address is dereferenced.

"Precise enough": For the $CESK_t^*$ machine, every function evaluates the same way, regardless of the stack. We should then represent the stack addresses as the components of a function call. The one place in the $CESK_t^*$ machine that continuations are allocated is at $(e_0\ e_1)$ evaluation. The expression itself, the environment, the store and the timestamp are necessary components for evaluating $(e_0\ e_1)$, so then we just represent the stack address as those four things. The stack is not relevant for its evaluation, so we do not want to store the stack addresses in the same store – that would also lead to a recursive heap structure. We will call this new table Ξ, because it looks like a stack.

By not storing the continuations in the value store, we separate "relevant" components from "irrelevant" components. We split the stack store from the value store and use only the value store in stack addresses. Stack addresses generally describe the relevant context that lead to their allocation, so we will refer to them henceforth as *contexts*. The resulting state space is updated here:

$$\begin{aligned} \widehat{State} &= \widehat{CESK}_t \times KStore \\ Storeable &= Value \\ \kappa \in Kont &::= \epsilon \mid \phi{:}\tau \\ \tau \in Context &::= \langle e, \rho, \sigma\rangle_t \\ \Xi \in KStore &= Context \rightharpoonup \wp(Kont) \end{aligned}$$

The semantics is modified slightly in Figure 5 to use Ξ instead of σ for continuation allocation and lookup. Given finite allocation, contexts are drawn from a finite space, but are still precise enough to describe an unbounded stack: they hold all the relevant components to find which stacks are possible. The computed $\longmapsto$ relation thus represents the full description of a pushdown system of reachable states (and the set of paths). Of course this semantics does not always define a pushdown system since $alloc$ can have an unbounded codomain. The correctness claim is therefore a correspondence between the same machine but with an unbounded stack, no Ξ, and $alloc, tick$ functions that behave the same disregarding

$\hat{\varsigma}, \Xi \longmapsto \hat{\varsigma}', \Xi' \quad a = alloc(\hat{\varsigma}, \Xi) \quad u = tick(\hat{\varsigma}, \Xi)$

$\langle x, \rho, \sigma, \hat{\kappa}\rangle_t, \Xi$	$\langle v, \sigma, \hat{\kappa}\rangle_u, \Xi$ if $v \in \sigma(\rho(x))$
$\langle (e_0\ e_1), \rho, \sigma, \hat{\kappa}\rangle_t, \Xi$	$\langle e_0, \rho, \sigma, \mathbf{appL}(e_1, \rho){:}\tau\rangle_u, \Xi'$
where	$\tau = \langle (e_0\ e_1), \rho, \sigma\rangle_t$
	$\Xi' = \Xi \sqcup [\tau \mapsto \hat{\kappa}]$
$\langle v, \sigma, \mathbf{appL}(e, \rho'){:}\tau\rangle_t, \Xi$	$\langle e, \rho', \sigma, \mathbf{appR}(v){:}\tau\rangle_u, \Xi$
$\langle v, \rho, \sigma, \mathbf{appR}(\lambda x.e, \rho'){:}\tau\rangle_t, \Xi$	$\langle e, \rho'', \sigma', \hat{\kappa}\rangle_u, \Xi$ if $\hat{\kappa} \in \Xi(\tau)$
where	$\rho'' = \rho'[x \mapsto a]$
	$\sigma' = \sigma \sqcup [a \mapsto v]$

Figure 5. $CESK_t^* \Xi$ semantics

$$\hat{\varsigma} \in \widehat{CESIK} = \langle e, \rho, \sigma, \iota, \hat{\kappa}\rangle \qquad \iota \in LKont = Frame^* \qquad \hat{\kappa} \in Kont ::= \epsilon \mid \tau$$

Figure 6. $CESIK^* \Xi$ semantic spaces

the different representations (a reasonable assumption). See the extended paper for details.

3.1 Engineered semantics for efficiency

We cover three optimizations that may be employed to accelerate the fixed-point computation.

1. Observe that Ξ can be made global with no loss in precision; it will not need to be stored in the frontier or set of seen states.
2. Continuations can be "chunked" more coarsely at function boundaries instead of at each frame in order to minimize table lookups.
3. Since evaluation is the same regardless of the stack, we can memoize results to short-circuit to the answer.

This last optimization will be covered in more detail in section 6. From here on, this paper will not explicitly mention timestamps.

A secondary motivation for the representation change in 2 is that flow analyses commonly split control-flow graphs at function call boundaries to enable the combination of intra- and inter-procedural analyses. In an abstract machine, this split looks like installing a continuation prompt at function calls. We borrow a representation from literature on delimited continuations [2] to split the continuation into two components: the continuation and meta-continuation. Our delimiters are special since each continuation "chunk" until the next prompt has bounded length. The bound is roughly the deepest nesting depth of an expression in functions' bodies. Instead of "continuation" and "meta-continuation" then, we will use terminology from CFA2 and call the top chunk a "local continuation," and the rest the "continuation."[3]

The resulting shuffling of the semantics to accommodate this new representation is in Figure 7. The extension to Ξ happens in a different rule – function entry – so the shape of the context changes to hold the function, argument, and store. We have a choice of whether to introduce an administrative step to dereference Ξ once ι is empty, or to use a helper metafunction to describe a "pop" of both ι and κ. Suppose we choose the second because the resulting semantics has a 1-to-1 correspondence with the previous semantics. A first attempt might land us here:

$$\begin{aligned} pop(\phi{:}\iota, \hat{\kappa}, \Xi) &= \{(\phi, \iota, \hat{\kappa})\} \\ pop(\epsilon, \tau, \Xi) &= \{(\phi, \iota, \hat{\kappa}) \ :\ (\phi{:}\iota, \hat{\kappa}) \in \Xi(\tau)\} \end{aligned}$$

[3] Since the continuation is either ϵ or a context, CFA2 calls these "entries" to mean execution entry into the program (ϵ) or a function (τ). One can also understand these as entries in a table (Ξ). We stay with the "continuation" nomenclature because they represent full continuations.

$$\hat{\varsigma}, \Xi \longmapsto \hat{\varsigma}', \Xi' \quad a = alloc(\hat{\varsigma}, \Xi)$$

$$\begin{array}{rl}
\langle x, \rho, \sigma, \iota, \hat{\kappa}\rangle, \Xi & \langle v, \sigma, \iota, \hat{\kappa}\rangle, \Xi \text{ if } v \in \sigma(\rho(x)) \\
\langle (e_0\ e_1), \rho, \sigma, \iota, \hat{\kappa}\rangle, \Xi & \langle e_0, \rho, \sigma, \mathbf{appL}(e_1, \rho){:}\iota, \hat{\kappa}\rangle, \Xi \\
\langle v, \sigma, \iota, \hat{\kappa}\rangle, \Xi & \langle e, \rho', \sigma, \mathbf{appR}(v, \rho){:}\iota', \hat{\kappa}'\rangle, \Xi \\
 & \text{if } \mathbf{appL}(e, \rho'), \iota', \hat{\kappa}' \in pop(\iota, \hat{\kappa}, \Xi) \\
\langle v, \sigma, \iota, \hat{\kappa}\rangle, \Xi & \langle e, \rho[x \mapsto a], \sigma', \epsilon, \tau\rangle, \Xi' \\
 & \text{if } \mathbf{appR}(\lambda x.e, \rho), \iota', \hat{\kappa}' \in pop(\iota, \hat{\kappa}, \Xi) \\
\text{where} & \sigma' = \sigma \sqcup [a \mapsto v] \\
 & \tau = (\langle \lambda x.e, \rho\rangle, v, \sigma) \\
 & \Xi' = \Xi \sqcup [\tau \mapsto (\iota, \hat{\kappa})]
\end{array}$$

Figure 7. $CESIK^*\Xi$ semantics

However, tail calls make the dereferenced τ lead to (ϵ, τ'). Because abstraction makes the store grow monotonically in a finite space, it's possible that $\tau' = \tau$ and a naive recursive definition of *pop* will diverge chasing these contexts. Now *pop* must save all the contexts it dereferences in order to guard against divergence. So $pop(\iota, \hat{\kappa}, \Xi) = pop^*(\iota, \hat{\kappa}, \Xi, \varnothing)$ where

$$\begin{aligned}
pop^*(\epsilon, \epsilon, \Xi, G) &= \varnothing \\
pop^*(\phi{:}\iota, \hat{\kappa}, \Xi, G) &= \{(\phi, \iota, \hat{\kappa})\} \\
pop^*(\epsilon, \tau, \Xi, G) &= \{(\phi, \iota, \hat{\kappa}) \ :\ (\phi{:}\iota, \hat{\kappa}) \in \Xi(\tau)\} \\
&\quad \cup \bigcup_{\tau' \in G'} pop^*(\epsilon, \tau', \Xi, G \cup G') \\
&\text{where } G' = \{\tau' \ :\ (\epsilon, \tau') \in \Xi(\tau)\} \setminus G
\end{aligned}$$

In practice, one would not expect G to grow very large. Had we chosen the first strategy, the issue of divergence is delegated to the machinery from the fixed-point computation.[4] However, when adding the administrative state, the "seen" check requires searching a far larger set than we would expect G to be.

$$\begin{aligned}
\mathcal{F}_e(S, R, F, \Xi) &= (S \cup F, R \cup R', F' \setminus S, \Xi') \\
I &= \bigcup_{s=(\hat{\varsigma}, \sigma) \in F} \{(\langle \hat{\varsigma}, \hat{\varsigma}'\rangle, \Xi') \ :\ \hat{\varsigma}, \Xi \longmapsto \hat{\varsigma}', \Xi'\} \\
R' &= \pi_0 I \qquad F' = \pi_1 R' \qquad \Xi' = \bigsqcup \pi_1 I
\end{aligned}$$

For a program e, we will say $(\varnothing, \varnothing, \{\langle e, \bot, \bot, \epsilon, \epsilon\rangle\}, \bot)$ is the bottom element of $\mathcal{F}_e$'s domain. The "analysis" then is then the pair of the R and Ξ components of $\mathbf{lfp}(\mathcal{F}_e)$.

4. Stack inspection and recursive metafunctions

Since we just showed how to produce a pushdown system from an abstract machine, some readers may be concerned that we have lost the ability to reason about the stack as a whole. This is not the case. The semantics may still refer to Ξ to make judgments about the possible stacks that can be realized at each state. The key is to interpret the functions making these judgments again with the AAM methodology.

Some semantic features allow a language to inspect some arbitrarily deep part of the stack, or compute a property of the whole stack before continuing. Java's access control security features are an example of the first form of inspection, and garbage collection is an example of the second. We will demonstrate both forms are simple first-order metafunctions that the AAM methodology will soundly interpret. Access control can be modeled with continuation marks, so we demonstrate with the CM machine of Clements and Felleisen.

Semantics that inspect the stack do so with metafunction calls that recur down the stack. Recursive metafunctions have a semantics as well, hence fair game for AAM. And, they should always terminate (otherwise the semantics is hosed). We can think of a simple pattern-matching recursive function as a set of rewrite rules that apply repeatedly until it reaches a result. Interpreted via AAM, non-deterministic metafunction evaluation leads to a set of possible results.

The finite restriction on the state space carries over to metafunction inputs, so we can always detect infinite loops that abstraction may have introduced and bail out of that execution path. Specifically, a metafunction call can be seen as an initial state, s, that will evaluate through the metafunction's rewrite rules $\longmapsto$ to compute all terminal states (outputs):

$$\begin{aligned}
terminal &: \forall A.\text{relation } A \times A \to \wp(A) \\
terminal(\longmapsto, s) &= terminal^*(\varnothing, \{s\}, \varnothing) \\
\text{where } terminal^*(S, \varnothing, T) &= T \\
terminal^*(S, F, T) &= terminal^*(S \cup F, F', T \cup T') \\
&\text{where } T' = \bigcup_{s \in F} post(s) \stackrel{?}{=} \varnothing \to \{s\}, \varnothing \\
&\qquad F' = \bigcup_{s \in F} post(s) \setminus S \\
post(s) &= \{s' \ :\ s \longmapsto s'\}
\end{aligned}$$

This definition is a typical worklist algorithm. It builds the set of terminal terms, T, by exploring the frontier (or worklist), F, and only adding terms to the frontier that have not been seen, as represented by S. If s has no more steps, $post(s)$ will be empty, meaning s should be added to the terminal set T. Note that it is possible for metafunctions' rewrite rules to themselves use metafunctions, but the *seen* set (S) for *terminal* must be dynamically bound[5] – it cannot restart at $\varnothing$ upon reentry. Without this precaution, the host language will exceed its stack limits when an infinite path is explored, rather than bail out.

4.1 Case study for stack traversal: GC

Garbage collection is an example of a language feature that needs to crawl the stack, specifically to find live addresses. We are interested in garbage collection because it can give massive precision boosts to analyses [11, 24]. The following function will produce the set of live addresses in the stack:

$$\begin{aligned}
K\mathcal{L}\mathcal{L} &: Frame^* \to \wp(Addr) \\
K\mathcal{L}\mathcal{L}(\hat{\kappa}) &= K\mathcal{L}\mathcal{L}^*(\hat{\kappa}, \varnothing) \\
K\mathcal{L}\mathcal{L}^*(\epsilon, L) &= L \\
K\mathcal{L}\mathcal{L}^*(\phi{:}\hat{\kappa}, L) &= K\mathcal{L}\mathcal{L}^*(\hat{\kappa}, L \cup \mathcal{T}(\phi)) \\
\text{where } \mathcal{T}(\mathbf{appL}(e, \rho)) &= \mathcal{T}(\mathbf{appR}(e, \rho)) = \mathcal{T}(e, \rho) \\
\mathcal{T}(e, \rho) &= \{\rho(x) \ :\ x \in fv(e)\}
\end{aligned}$$

When interpreted via AAM, the continuation is indirected through Ξ and leads to multiple results, and possibly loops through Ξ. Thus this is more properly understood as

$$\begin{aligned}
K\mathcal{L}\mathcal{L}(\Xi, \hat{\kappa}) &= terminal(\longmapsto, K\mathcal{L}\mathcal{L}^*(\Xi, \hat{\kappa}, \varnothing)) \\
K\mathcal{L}\mathcal{L}^*(\Xi, \epsilon, L) &\longmapsto L \\
K\mathcal{L}\mathcal{L}^*(\Xi, \phi{:}\tau, L) &\longmapsto K\mathcal{L}\mathcal{L}^*(\Xi, \hat{\kappa}, L \cup \mathcal{T}(\phi)) \text{ if } \hat{\kappa} \in \Xi(\tau)
\end{aligned}$$

A garbage collecting semantics can choose to collect the heap with respect to each live set (call this Γ^*), or, soundly, collect with respect to their union (call this $\hat{\Gamma}$).[6] On the one hand we could have

[4] CFA2 employs the first strategy and calls it "transitive summaries."

[5] This is a reference to dynamic scope as opposed to lexical scope.

[6] The garbage collecting version of PDCFA [17] evaluates the $\hat{\Gamma}$ strategy.

tighter collections but more possible states, and on the other hand we can leave some precision behind in the hope that the state space will be smaller. In the general idea of relevance versus irrelevance, the continuation's live addresses are relevant to execution, but are already implicitly represented in contexts because they must be mapped in the store's domain.

A state is "collected" only if live addresses remain in the domain of σ. We say a value $v \in \sigma(a)$ is live if a is live. If a value is live, any addresses it touches are live; this is captured by the computation in $\mathcal{R}$:

$$\mathcal{R}(root, \sigma) = \{b \ : \ a \in root, a \rightsquigarrow_\sigma^* b\}$$

$$\frac{v \in \sigma(a) \qquad b \in \mathcal{T}(v)}{a \rightsquigarrow_\sigma b}$$

So the two collection methods are as follows. Exact GC produces different collected states based on the possible stacks' live addresses:[7]

$$\Gamma^*(\hat{\varsigma}, \Xi) = \{\hat{\varsigma}\{\sigma := \hat{\varsigma}.\sigma|_L\} \ : \ L \in \mathit{live}^*(\hat{\varsigma}, \Xi)\}$$

$$\mathit{live}^*(\langle e, \rho, \sigma, \hat{\kappa}\rangle, \Xi) = \{\mathcal{R}(\mathcal{T}(e, \rho) \cup L, \sigma) \ : \ L \in K\mathcal{LL}(\Xi, \hat{\kappa})\}$$

$$\frac{\hat{\varsigma}, \Xi \longmapsto \hat{\varsigma}', \Xi' \qquad \hat{\varsigma}' \in \Gamma^*(\varsigma', \Xi')}{\hat{\varsigma}, \Xi \longmapsto_{\Gamma^*} \hat{\varsigma}', \Xi}$$

And inexact GC produces a single state that collects based on all (known) stacks' live addresses:

$$\hat{\Gamma}(\hat{\varsigma}, \Xi) = \hat{\varsigma}\{\sigma := \hat{\varsigma}.\sigma|_{\widehat{\mathit{live}}(\hat{\varsigma}, \Xi)}\}$$

$$\widehat{\mathit{live}}(\langle e, \rho, \sigma, \hat{\kappa}\rangle, \Xi) = \mathcal{R}(\mathcal{T}(e, \rho) \cup \bigcup K\mathcal{LL}(\Xi, \hat{\kappa}), \sigma)$$

$$\frac{\hat{\varsigma}, \Xi \longmapsto \hat{\varsigma}', \Xi'}{\hat{\varsigma}, \Xi \longmapsto_{\hat{\Gamma}} \hat{\Gamma}(\varsigma', \Xi')\Xi'}$$

Without the continuation store, the baseline GC is

$$\Gamma(\hat{\varsigma}) = \hat{\varsigma}\{\sigma := \hat{\varsigma}.\sigma|_{\mathit{live}(\hat{\varsigma})}\}$$

$$\mathit{live}(e, \rho, \sigma, \kappa) = \mathcal{R}(\mathcal{T}(e, \rho) \cup K\mathcal{LL}(\hat{\kappa}), \sigma)$$

$$\frac{\hat{\varsigma} \longmapsto \hat{\varsigma}'}{\hat{\varsigma} \longmapsto_\Gamma \Gamma(\varsigma')}$$

Suppose at arbitrary times we decide to perform garbage collection rather than continue with garbage. So when $\hat{\varsigma} \longmapsto \hat{\varsigma}'$, we instead do $\hat{\varsigma} \longmapsto_\Gamma \hat{\varsigma}'$. The times we perform GC do not matter for soundness, since we are not analyzing GC behavior. However, garbage stands in the way of completeness. Mismatches in the GC application for the different semantics lead to mismatches in resulting state spaces, not just up to garbage in stores, but in spurious paths from dereferencing a reallocated address that was not first collected.

4.2 Case study analyzing security features: the CM machine

The CM machine provides a model of access control: a dynamic branch of execution is given permission to use some resource if a continuation mark for that permission is set to "grant." There are three new forms we add to the lambda calculus to model this feature: `grant`, `frame`, and `test`. The `grant` construct adds a permission to the stack. The concern of unforgeable permissions is orthogonal, so we simplify with a set of permissions that is textually present in the program:

$P \in \mathit{Permissions}$ a set

$$\mathit{Expr} ::= \ldots \mid \texttt{grant}\ P\ e \mid \texttt{frame}\ P\ e \mid \texttt{test}\ P\ e\ e$$

[7] It is possible and more efficient to build the stack's live addresses piecemeal as an additional component of each state, precluding the need for $K\mathcal{LL}$. Each stack in Ξ would also store the live addresses to restore on pop.

$\langle \texttt{grant}\ P\ e, \rho, \sigma, \kappa\rangle$	$\langle e, \rho, \sigma, \kappa[P \mapsto \mathbf{Grant}]\rangle$
$\langle \texttt{frame}\ P\ e, \rho, \sigma, \kappa\rangle$	$\langle e, \rho, \sigma, \kappa[\overline{P} \mapsto \mathbf{Deny}]\rangle$
$\langle \texttt{test}\ P\ e_0\ e_1, \rho, \sigma, \kappa\rangle$	$\langle e_0, \rho, \sigma, \kappa\rangle$ if $\mathit{True} = \mathcal{OK}(P, \kappa)$
	$\langle e_1, \rho, \sigma, \kappa\rangle$ if $\mathit{False} = \mathcal{OK}(P, \kappa)$

Figure 8. CM machine semantics

The `frame` construct ensures that only a given set of permissions are allowed, but not necessarily granted. The security is in the semantics of `test`: we can test if all marks in some set P have been granted in the stack without first being denied; this involves crawling the stack:

$$\mathcal{OK}(\varnothing, \kappa) = \mathit{True}$$

$$\mathcal{OK}(P, \epsilon^m) = \mathit{pass?}(P, m)$$

$$\mathcal{OK}(P, \phi{:}^m\kappa) = \mathit{pass?}(P, m)\ \textbf{and}\ \mathcal{OK}(P \setminus m^{-1}(\mathbf{Grant}), \kappa)$$

where $\mathit{pass?}(P, m) = P \cap m^{-1}(\mathbf{Deny}) \stackrel{?}{=} \varnothing$

The set subtraction is to say that granted permissions do not need to be checked farther down the stack.

Continuation marks respect tail calls and have an interface that abstracts over the stack implementation. Each stack frame added to the continuation carries the permission map. The empty continuation also carries a permission map. Crucially, the added constructs do not add frames to the stack; instead, they update the permission map in the top frame, or if empty, the empty continuation's permission map.

$$m \in \mathit{PermissionMap} = \mathit{Permissions} \rightharpoonup \mathit{GD}$$

$$\mathit{gd} \in \mathit{GD} ::= \mathbf{Grant} \mid \mathbf{Deny}$$

$$\kappa \in \mathit{Kont} ::= \epsilon^m \mid \phi{:}^m\kappa$$

Update for continuation marks:

$$m[P \mapsto \mathit{gd}] = \lambda x.x \stackrel{?}{\in} P \to \mathit{gd}, m(x)$$

$$m[\overline{P} \mapsto \mathit{gd}] = \lambda x.x \stackrel{?}{\in} P \to m(x), \mathit{gd}$$

The abstract version of the semantics has one change on top of the usual continuation store. The `test` rules are now

$$\langle \texttt{test}\ P\ e_0\ e_1, \rho, \sigma, \hat{\kappa}\rangle, \Xi \longmapsto \langle e_0, \rho, \sigma, \hat{\kappa}\rangle, \Xi \text{ if } \mathit{True} \in \widehat{\mathcal{OK}}(\Xi, P, \hat{\kappa})$$

$$\longmapsto \langle e_1, \rho, \sigma, \hat{\kappa}\rangle, \Xi \text{ if } \mathit{False} \in \widehat{\mathcal{OK}}(\Xi, P, \hat{\kappa})$$

where the a new $\widehat{\mathcal{OK}}$ function uses *terminal* and rewrite rules:

$$\widehat{\mathcal{OK}}(\Xi, P, \hat{\kappa}) = \mathit{terminal}(\longmapsto, \widehat{\mathcal{OK}}^*(\Xi, P, \hat{\kappa}))$$

$$\widehat{\mathcal{OK}}^*(\Xi, \varnothing, \hat{\kappa}) \longmapsto \mathit{True}$$

$$\widehat{\mathcal{OK}}^*(\Xi, P, \epsilon^m) \longmapsto \mathit{pass?}(P, m)$$

$$\widehat{\mathcal{OK}}^*(\Xi, P, \phi{:}^m\tau) \longmapsto b \text{ where}$$

$$b \in \{\mathit{pass?}(P, m)\ \textbf{and}\ b \ : \ \hat{\kappa} \in \Xi(\tau),$$

$$b \in \widehat{\mathcal{OK}}(\Xi, P \setminus m^{-1}(\mathbf{Grant}), \hat{\kappa}))\}$$

This definition works fine with the reentrant *terminal* function with a dynamically bound *seen* set, but otherwise needs to be rewritten to accumulate the Boolean result as a parameter of $\widehat{OK}^*$.

5. Relaxing contexts for delimited continuations

In section 3 we showed how to get a pushdown abstraction by separating continuations from the heap that stores values. This separation breaks down when continuations themselves become values via first-class control operators. The glaring issue is that continuations become "storeable" and relevant to the execution of functions.

$$\varsigma \longmapsto_{SR} \varsigma'$$

$\mathbf{ev}(\texttt{reset}\ e, \rho, \sigma, \kappa, C)$	$\mathbf{ev}(e, \rho, \sigma, \epsilon, \kappa \circ C)$
$\mathbf{co}(\epsilon, \kappa \circ C, v, \sigma)$	$\mathbf{co}(\kappa, C, v, \sigma)$
$\mathbf{ev}(\texttt{shift}\ x.e, \rho, \sigma, \kappa, C)$	$\mathbf{ev}(e, \rho[x \mapsto a], \sigma', \epsilon, C)$
where	$\sigma' = \sigma \sqcup [a \mapsto \mathbf{comp}(\kappa)]$
$\mathbf{co}(\mathbf{fn}(\mathbf{comp}(\kappa')) : \kappa, C, v, \sigma)$	$\mathbf{co}(\kappa', \kappa \circ C, v, \sigma)$

Figure 9. Machine semantics for shift/reset

$$\begin{aligned}
\hat{\varsigma} \in \widehat{SR} &::= \mathbf{ev}(e, \rho, \hat{\sigma}, \chi, \hat{\kappa}, \hat{C}) \mid \mathbf{co}(\hat{\kappa}, \hat{C}, \hat{v}, \hat{\sigma}, \chi) \\
State &::= \hat{\varsigma}, \Xi_{\hat{\kappa}}, \Xi_{\hat{C}} \\
\chi \in KClosure &= Addr \rightharpoonup \wp(Store) \\
\Xi_{\hat{\kappa}} \in KStore &= ExactContext \rightharpoonup \wp(\widehat{Kont}) \\
\Xi_{\hat{C}} \in CStore &= MContext \rightharpoonup \wp(\widehat{Kont} \times \widehat{MKont}) \\
\hat{\kappa} \in \widehat{Kont} &::= \epsilon \mid \phi{:}\tau \mid \tau \qquad \tilde{\kappa} \in \widetilde{Kont} ::= \epsilon \mid \hat{\tau} \\
\hat{C} \in \widehat{MKont} &::= \epsilon \mid \gamma \qquad \hat{v} \in \widehat{Value} ::= \tilde{\kappa} \mid (\ell, \rho)
\end{aligned}$$

Figure 10. Shift/reset abstract semantic spaces

But, it was precisely the *irrelevance* that allowed the separation of σ and Ξ. Specifically, the store components of continuations become elements of the store's codomain — a recursion that can lead to an unbounded state space and therefore a non-terminating analysis. We apply the AAM methodology to cut out the recursion; whenever a continuation is captured to go into the store, we allocate an address to approximate the store component of the continuation.

We introduce a new environment, χ, that maps these addresses to the stores they represent. The stores that contain addresses in χ are then *open*, and must be paired with χ to be *closed*. This poses the same problem as before with contexts in storeable continuations. Therefore, we give up some precision to regain termination by *flattening* these environments when we capture continuations. Fresh allocation still maintains the concrete semantics, but we necessarily lose some ability to distinguish contexts in the abstract.

5.1 Case study of first-class control: shift and reset

We choose to study `shift` and `reset` [9] because delimited continuations have proven useful for implementing web servers [22, 27], providing processes isolation in operating systems [21], representing computational effects [13], modularly implementing error-correcting parsers [32], and finally undelimited continuations are *passé* for good reason [20]. Even with all their uses, however, their semantics can yield control-flow possibilities that surprise their users. A *precise* static analysis that illuminates their behavior is then a valuable tool.

Our concrete test subject is the abstract machine for shift and reset adapted from Biernacki et al. [2] in the "**ev**al, **co**ntinue" style in Figure 9. The figure elides the rules for standard function calls. The new additions to the state space are a new kind of value, $\mathbf{comp}(\kappa)$, and a *meta-continuation*, $C \in MKont = Kont^*$ for separating continuations by their different prompts. Composable continuations are indistinguishable from functions, so even though the meta-continuation is concretely a list of continuations, its conses are notated as function composition: $\kappa \circ C$.

5.2 Reformulated with continuation stores

The machine in Figure 9 is transformed now to have three new tables: one for continuations, one as discussed in the section beginning to close stored continuations, and one for meta-continuations. The first is like previous sections, albeit continuations may now have the approximate form that is storeable. The meta-continuation table is more like previous sections because meta-contexts are not storeable. Meta-continuations do not have simple syntactic strategies for bounding their size, so we choose to bound them to size 0. They could be paired with lists of $\widehat{Kont}$ bounded at an arbitrary $n \in \mathbb{N}$, but we simplify for presentation.

Contexts for continuations are still at function application, but now contain the χ. Contexts for meta-continuations are in two places: manual prompt introduction via `reset`, or via continuation invocation. At continuation capture time, continuation contexts are approximated to remove $\hat{\sigma}$ and χ components. The different context spaces are thus:

$$\begin{aligned}
\dot{\tau} \in ExactContext &::= \langle e, \rho, \hat{\sigma}, \chi \rangle \\
\hat{\tau} \in \widehat{Context} &::= \langle e, \rho, a \rangle \\
\tau \in Context &::= \hat{\tau} \mid \dot{\tau} \\
\gamma \in MContext &::= \langle e, \rho, \hat{\sigma}, \chi \rangle \mid \langle \tilde{\kappa}, \hat{v}, \hat{\sigma}, \chi \rangle
\end{aligned}$$

The approximation and flattening happens in $\mathbb{A}$:

$$\mathbb{A} : KClosure \times Addr \times \widehat{Kont} \to KClosure \times \widetilde{Kont}$$

$$\begin{aligned}
\mathbb{A}(\chi, a, \epsilon) &= \chi, \epsilon \\
\mathbb{A}(\chi, a, \phi{:}\tau) &= \chi', \phi{:}\hat{\tau} \text{ where } (\chi', \hat{\tau}) = \mathbb{A}(\chi, a, \tau) \\
\mathbb{A}(\chi, a, \langle e, \rho, \hat{\sigma}, \chi' \rangle) &= \chi \sqcup \chi' \sqcup [a \mapsto \hat{\sigma}], \phi{:}\langle e, \rho, a \rangle \\
\mathbb{A}(\chi, a, \langle e, \rho, b \rangle) &= \chi \sqcup [a \mapsto \chi(b)], \phi{:}\langle e, \rho, a \rangle
\end{aligned}$$

The third case is where continuation closures get flattened together. The fourth case is when an already approximate continuation is approximated: the approximation is inherited. Approximating the context and allocating the continuation in the store require two addresses, so we relax the specification of *alloc* to allow multiple address allocations in this case.

Each of the four rules of the original shift/reset machine has a corresponding rule that we explain piecemeal. We will use $\dashrightarrow$ for steps that do not modify the continuation stores for notational brevity. We use the above $\mathbb{A}$ function in the rule for continuation capture, as modified here.

$$\mathbf{ev}(\texttt{shift}\ x.e, \rho, \hat{\sigma}, \chi, \hat{\kappa}, \hat{C}) \dashrightarrow \mathbf{ev}(e, \rho', \hat{\sigma}', \chi', \epsilon, \hat{C})$$

where

$$\begin{aligned}
(a, a') &= alloc(\hat{\varsigma}, \Xi_{\hat{\kappa}}, \Xi_{\hat{C}}) & \rho' &= \rho[x \mapsto a] \\
(\chi', \tilde{\kappa}) &= \mathbb{A}(\chi, a', \hat{\kappa}) & \hat{\sigma}' &= \hat{\sigma} \sqcup [a \mapsto \tilde{\kappa}]
\end{aligned}$$

The rule for `reset` stores the continuation and meta-continuation in $\Xi_{\hat{C}}$:

$$\begin{aligned}
\mathbf{ev}(\texttt{reset}\ e, \rho, \hat{\sigma}, \chi, \hat{\kappa}, \hat{C}), \Xi_{\hat{\kappa}}, \Xi_{\hat{C}} \longmapsto \mathbf{ev}(e, \rho, \hat{\sigma}, \chi, \epsilon, \gamma), \Xi_{\hat{\kappa}}, \Xi'_{\hat{C}} \\
\text{where } \gamma = \langle e, \rho, \hat{\sigma}, \chi \rangle \\
\Xi_{\hat{C}} = \Xi_{\hat{C}} \sqcup [\gamma \mapsto (\hat{\kappa}, \hat{C})]
\end{aligned}$$

The prompt-popping rule simply dereferences $\Xi_{\hat{C}}$:

$$\mathbf{co}(\epsilon, \gamma, \hat{v}, \hat{\sigma}, \chi) \dashrightarrow \mathbf{co}(\hat{\kappa}, \hat{C}, \hat{v}, \hat{\sigma}, \chi) \text{ if } (\hat{\kappa}, \hat{C}) \in \Xi_{\hat{C}}(\gamma)$$

The continuation installation rule extends $\Xi_{\hat{C}}$ at the different context:

$$\begin{aligned}
\mathbf{co}(\hat{\kappa}, \hat{C}, \hat{v}, \hat{\sigma}, \chi), \Xi_{\hat{\kappa}}, \Xi_{\hat{C}} \longmapsto \mathbf{co}(\tilde{\kappa}, \gamma, \hat{v}, \hat{\sigma}, \chi), \Xi_{\hat{\kappa}}, \Xi'_{\hat{C}} \\
\text{if } (\mathbf{appR}(\tilde{\kappa}), \hat{\kappa}') \in pop(\Xi_{\hat{\kappa}}, \chi, \hat{\kappa}) \\
\text{where } \gamma = \langle \tilde{\kappa}, \hat{v}, \hat{\sigma}, \chi \rangle \\
\Xi_{\hat{C}} = \Xi_{\hat{C}} \sqcup [\gamma \mapsto (\hat{\kappa}', \hat{C})]
\end{aligned}$$

Again we have a metafunction pop, but this time to interpret approximated continuations:

$$
\begin{aligned}
pop(\Xi_{\hat{\kappa}}, \chi, \hat{\kappa}) &= pop^*(\hat{\kappa}, \varnothing) \\
\text{where } pop^*(\epsilon, G) &= \varnothing \\
pop^*(\phi{:}\tau, G) &= \{(\phi, \tau)\} \\
pop^*(\tau, G) &= \bigcup_{\hat{\kappa} \in G'} (pop^*(\hat{\kappa}, G \cup G')) \\
\text{where } G' &= \bigcup_{\dot{\tau} \in I(\tau)} \Xi_{\hat{\kappa}}(\dot{\tau}) \setminus G \\
I(\dot{\tau}) &= \{\dot{\tau}\} \\
I(\langle e, \rho, a \rangle) &= \{\langle e, \rho, \hat{\sigma}, \chi' \rangle \in \mathrm{dom}(\Xi_{\hat{\kappa}}) \ : \ \hat{\sigma} \in \chi(a), \chi' \sqsubseteq \chi\}
\end{aligned}
$$

Notice that since we flatten χs together, we need to compare for containment rather than for equality (in I). A variant of this semantics with GC is available in the PLT redex models.

Comparison to CPS transform to remove `shift` ***and*** `reset`***:*** We lose precision if we use a CPS transform to compile away `shift` and `reset` forms, because variables are treated less precisely than continuations. Consider the following program and its CPS transform for comparison:

```
(let* ([id (λ (x) x)]
       [f (λ (y) (shift k (k (k y))))]
       [g (λ (z) (reset (id (f z))))])
  (≤ (g 0) (g 1)))

(let* ([id (λ (x k) (k x))]
       [f (λ (y j) (j (j y)))]
       [g (λ (z h)
            (h (f z (λ (fv)
                      (id fv (λ (i) i)))))))])
  (g 0 (λ (g0v) (g 1 (λ (g1v) (≤ g0v g1v))))))
```

The $CESK^*_t\Xi$ machine with a monovariant allocation strategy will predict the CPS'd version returns true or false. In analysis literature, "monovariant" means variables get one address, namely themselves. Our specialized analysis for delimited control will predict the non-CPS'd version returns true.

6. Short-circuiting via "summarization"

All the semantics of previous sections have a performance weakness that many analyses share: unnecessary propagation. Consider two portions of a program that do not affect one another's behavior. Both can change the store, and the semantics will be unaware that the changes will not interfere with the other's execution. The more possible stores there are in execution, the more possible contexts in which a function will be evaluated. Multiple independent portions of a program may be reused with the same arguments and store contents they depend on, but changes to irrelevant parts of the store lead to redundant computation. The idea of skipping from a change past several otherwise unchanged states to uses of the change is called "sparseness" in the literature [26, 28, 38].

Memoization is a specialized instance of sparseness; the base stack may change, but the evaluation of the function does not, so given an already computed result we can jump straight to the answer. We use the vocabulary of "relevance" and "irrelevance" so that future work can adopt the ideas of sparseness to reuse contexts in more ways.

Recall the context irrelevance lemma: if we have seen the results of a computation before from a different context, we can reuse them. The semantic counterpart to this idea is a memo table that we extend when popping and appeal to when about to push. This simple idea works well with a deterministic semantics, but the nondeterminism of abstraction requires care. In particular, memo table entries can end up mapping to multiple results, but not all results will be found at the same time. Note the memo table space:

$$
\begin{aligned}
M \in Memo &= Context \rightharpoonup \wp(Relevant) \\
Relevant &::= \langle e, \rho, \sigma \rangle
\end{aligned}
$$

There are a few ways to deal with multiple results:

1. rerun the analysis with the last memo table until the table doesn't change (expensive),
2. short-circuit to the answer but also continue evaluating anyway (negates most benefit of short-circuiting), or
3. use a frontier-based semantics like in subsection 3.1 with global Ξ and M, taking care to
   (a) at memo-use time, still extend Ξ so later memo table extensions will "flow" to previous memo table uses, and
   (b) when Ξ and M are extended at the same context at the same time, also create states that act like the M extension point also returned to the new continuations stored in Ξ.

We will only discuss the final approach. The same result can be achieved with a one-state-at-a-time frontier semantics, but we believe this is cleaner and more parallelizable. Its second subpoint we will call the "push/pop rendezvous." The rendezvous is necessary because there may be no later push or pop steps that would regularly appeal to either (then extended) table at the same context. The frontier-based semantics then makes sure these pushes and pops find each other to continue on evaluating. In pushdown and nested word automata literature, the push to pop short-circuiting step is called a "summary edge" or with respect to the entire technique, "summarization." We find the memoization analogy appeals to programmers' and semanticists' operational intuitions.

A second concern for using memo tables is soundness. Without the completeness property of the semantics, memoized results in, *e.g.*, an inexactly GC'd machine, can have dangling addresses since the possible stacks may have grown to include addresses that were previously garbage. These addresses would not be garbage at first, since they must be mapped in the store for the contexts to coincide, but during the function evaluation the addresses can become garbage. If they are supposed to then be live, and are used (presumably they are reallocated post-collection), the analysis will miss paths it must explore for soundness.

The rules in Figure 11 are the importantly changed rules from section 3 that short-circuit to memoized results. The technique looks more like memoization with a $CESIK^*_t\Xi$ machine, since the memoization points are truly at function call and return boundaries. The pop function would need to also update M if it dereferences through a context, but otherwise the semantics are updated *mutatis mutandis*.

$$\hat{\varsigma}, \Xi, M \longmapsto \hat{\varsigma}', \Xi', M'$$

$\langle (e_0\ e_1), \rho, \sigma, \hat{\kappa} \rangle, \Xi, M$	$\langle e_0, \rho, \sigma, \mathbf{appL}(e_1, \rho){:}\tau \rangle, \Xi, M$ if $\tau \notin \mathrm{dom}(M)$, or $\langle e', \rho', \sigma', \hat{\kappa} \rangle, \Xi', M$ if $\langle e', \rho', \sigma' \rangle \in M(\tau)$
where	$\tau = \langle (e_0\ e_1), \rho, \sigma \rangle$ $\Xi' = \Xi \sqcup [\tau \mapsto \hat{\kappa}]$
$\langle v, \sigma, \mathbf{appR}(\lambda x.e, \rho){:}\tau \rangle, \Xi, M$	$\langle e, \rho', \sigma', \hat{\kappa} \rangle, \Xi, M'$ if $\hat{\kappa} \in \Xi(\tau)$
where	$\rho' = \rho[x \mapsto a]$ $\sigma' = \sigma \sqcup [a \mapsto v]$ $M' = M \sqcup [\tau \mapsto \langle e, \rho', \sigma' \rangle]$

Figure 11. Important memoization rules

$$\mathcal{F}_e(S, R, F, \Xi, M) = (S \cup F, R \cup R', F' \setminus S, \Xi', M')$$

where

$$I = \bigcup_{\varsigma \in F} \{(\langle \varsigma, \varsigma' \rangle, \Xi', M') \ : \ \varsigma, \Xi, M \longmapsto \varsigma', \Xi', M'\}$$
$$R' = \pi_0 I \qquad \Xi' = \bigsqcup \pi_1 I \qquad M' = \bigsqcup \pi_2 I$$
$$\Delta\Xi = \Xi' \setminus \Xi \qquad \Delta M = M' \setminus M$$
$$F' = \pi_1 R' \cup \{\langle e, \rho, \sigma, \hat{\kappa} \rangle : \tau \in \mathrm{dom}(\Delta\Xi) \cap \mathrm{dom}(\Delta M). \hat{\kappa} \in \Delta\Xi(\tau), \langle e, \rho, \sigma \rangle \in \Delta M(\tau)\}$$

7. Related Work

The immediately related work is that of PDCFA [10, 11], CFA2 [36, 37], and AAM [33]. The stack frames in CFA2 that boost precision are an orthogonal feature that fit into our model as an *irrelevant* component along with the stack, which we did not cover in detail due to space constraints. The version of CFA2 that handles `call/cc` does not handle composable control, is dependent on a restricted CPS representation, and has untunable precision for first-class continuations. Our semantics adapts to `call/cc` by removing the meta-continuation operations, and thus this work supersedes theirs; the machinery is in fact a strict generalization. The extended version of PDCFA that inspects the stack to do garbage collection [11] also fits into our model (subsection 4.1's $\hat{\Gamma}$).

We did additional work to improve the performance of the AAM approach in Johnson et al. [16] that can almost entirely be imported for the work in this paper. The technique that does not apply is "store counting" for lean representation of the store component of states when there is a global abstract store, an assumption that does not hold for garbage collection. The implementation[8] has pushdown modules that use the ideas in this paper.

Stack inspection Stack inspecting flow analyses also exist, but operate on pre-constructed regular control-flow graphs [1], so the CFGs cannot be trimmed due to the extra information at construction time, leading to less precision. Backward analyses for stack inspection also exist, with the same prerequisite [4].

Pushdown models and memoization The idea of relating pushdown automata with memoization is not new. In 1971, Stephen Cook [6] devised a transformation to simulate 2-way (on a fixed input) *deterministic* pushdown automata in time linear in the size of the input, that uses the same "context irrelevance" idea to skip from state q seen before to a corresponding first state that pops the stack below where q started (called a *terminator* state). Six years later, Neil D. Jones [18] simplified the transformation instead to *interpret* a stack machine program to work *on-the-fly* still on a deterministic machine, but with the same idea of using memo tables to remember corresponding terminator states. Thirty-six years after that, at David Schmidt's Festschrift, Robert Glück extended the technique to 2-way *non-deterministic* pushdown automata, and showed that the technique can be used to recognize context-free languages in the standard $\mathcal{O}(n^3)$ time [14]. Glück's technique (as yet, correctness unproven) uses the meta-language's stack with a deeply recursive interpretation function to preclude the use of a frontier and something akin to Ξ[9]. By exploring the state space *depth-first*, the interpreter can find all the different terminators a state can reach one-by-one by destructively updating the memo table with the "latest" terminator found. The trade-offs with this technique are that it does not obviously scale to first-class control, and the search can overflow the stack when interpreting moderate-sized programs. A minor disadvantage is that it is also not a fair evaluation strategy when allocation is unbounded. The technique can nevertheless be a viable alternative for languages with simple control-flow mechanisms. It has close similarities to "Big-CFA2" in Vardoulakis' dissertation [35].

Analysis of pushdown automata Pushdown models have existed in the first-order static analysis literature [25, Chapter 7][29], and the first-order model checking literature [3], for some time. These methods already assume a pushdown model as input, and constructing a model from a first-order program is trivial. In the setting of higher-order functions and first-class control, model construction is an additional problem – the one we solve here. Additionally, the algorithms employed in these works expect a complete description of the model up front, rather than work with a modified `step` function (also called `post`), such as in "on-the-fly" model-checking algorithms for finite state systems [31].

Derivation from abstract machines The trend of deriving static analyses from abstract machines does not stop at flow analyses. The model-checking community showed how to check temporal logic queries for collapsible pushdown automata (CPDA), or equivalently, higher-order recursion schemes, by deriving the checking algorithm from the Krivine machine [30]. The expressiveness of CPDAs outweighs that of PDAs, but it is unclear how to adapt higher-order recursion schemes (HORS) to model arbitrary programming language features. The method is strongly tied to the simply-typed call-by-name lambda calculus and depends on finite sized base-types.

8. Conclusion

As the programming world continues to embrace behavioral values like functions and continuations, it becomes more important to import the powerful techniques pioneered by the first-order analysis and model-checking communities. It is our view that systematic approaches to analysis construction are pivotal to scaling them to production programming languages. We showed how to systematically construct executable concrete semantics that point-wise abstract to pushdown analyses of higher-order languages. We bypass the automata theoretic approach so that we are not chained to a pushdown automaton to model features such as first-class composable control operators. The techniques employed for pushdown analysis generalize gracefully to apply to non-pushdown models and give better precision than regular methods.

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